

## Pin Details of HC-05 Bluetooth Hardware Package

PIN Name	PIN #	PAD Type	Description
<b>GND</b>	13,21,22	VSS	Ground Pot
<b>3.3 VCC</b>	12	3.3V	Integrated 3.3V(+) supply with On-chip linear regulator output within 3.15-3.3V
<b>AIO0</b>	9	Bi-directional	Programmable input/output line
<b>AIO1</b>	10	Bi-directional	Programmable input/output line
<b>AIO0</b>	23	Bi-directional RX EN	Programmable input/output line, control output for LNA (if fitted)
<b>AIO1</b>	24	Bi-directional TX EN	Programmable input/output line, control output for PA (if fitted)

PIN Name	PIN #	PAD Type	Description
<b>PIO2</b>	25	Bi-directional	Programmable input/output line
<b>PIO3</b>	26	Bi-directional	Programmable input/output line
<b>PIO4</b>	27	Bi-directional	Programmable input/output line
<b>PIO5</b>	28	Bi-directional	Programmable input/output line
<b>PIO6</b>	29	Bi-directional	Programmable input/output line
<b>PIO7</b>	30	Bi-directional	Programmable input/output line
<b>PIO8</b>	31	Bi-directional	Programmable input/output line
<b>PIO9</b>	32	Bi-directional	Programmable input/output line
<b>PIO10</b>	33	Bi-directional	Programmable input/output line
<b>PIO11</b>	34	Bi-directional	Programmable input/output line

<b>PIN Name</b>	<b>PIN #</b>	<b>PAD Type</b>	<b>Description</b>
<b>RESETB</b>	11	CMOS input with weak internal pull-up	Reset of low. Input denounced so must be low for >5MS to cause a reset
<b>UART_RTS</b>	4	CMOS output, tri-stable with weak internal pull-up	UART request to send, active low
<b>UART_CTS</b>	3	CMOS input with weak internal pull-down	UART clear to send, active low
<b>UART_RX</b>	2	CMOS input with weak internal pull-down	UART Data input
<b>UART_TX</b>	1	CMOS output, tri-stable with weak internal pull-up	UART Data output
<b>SPI_MOSI</b>	17	CMOS input with weak internal pull-down	Serial peripheral interface data input

<b>PIN Name</b>	<b>PIN #</b>	<b>PAD Type</b>	<b>Description</b>
<b>SPI_CSB</b>	16	CMOS input with weak internal pull-up	Chip select for serial peripheral interface, active low
<b>SPI_CLK</b>	19	CMOS input with weak internal pull-down	Serial peripheral interface clock
<b>SPI_MISO</b>	18	CMOS input with weak internal pull-down	Serial peripheral interface data output
<b>USB_-</b>	15	Bi-directional	
<b>USB_+</b>	20	Bi-directional	
<b>NC</b>	14		
<b>PCM_CLK</b>	5	Bi-directional	Synchronous PCM data clock
<b>PCM_OUT</b>	6	CMOS output	Synchronous PCM data output
<b>PCM_IN</b>	7	CMOS input	Synchronous PCM data input
<b>PCM_SYNC</b>	8	Bi-directional	Synchronous PCM data strobe